## **Data sheet**

6ES7314-6BH04-0AB0



SIMATIC S7-300, CPU 314C-2 PTP Compact CPU with MPI, 24 DI/16 DO, 4 AI, 2 AO, 1 Pt100, 4 high-speed counters (60 kHz), integrated interface RS485, Integr. power supply 24 V DC, work memory 192 KB, Front connector (2x 40-pole) and Micro Memory Card required

CPU 314C-2 PtP
01
V3.3
STEP 7 as of V5.5 + SP1 or STEP 7 V5.3 + SP2 or higher with HSP 204
24 V
19.2 V
28.8 V
Miniature circuit breaker, type C; min. 2 A; miniature circuit breaker type B, min. 4 A
5 ms
1 s
24 V
Yes
24 V
No
660 mA
150 mA
5 A
0.7 A²-s
80 mA
50 mA
13 W
192 kbyte
No
Yes

<ul> <li>Data management on MMC (after last programming), min.</li> </ul>	10 a
Backup	
• present	Yes; Guaranteed by MMC (maintenance-free)
without battery	Yes; Program and data
PU processing times	
for bit operations, typ.	0.06 μs
for word operations, typ.	0.12 µs
for fixed point arithmetic, typ.	0.16 µs
for floating point arithmetic, typ.	0.59 µs
PU-blocks	
Number of blocks (total)	1 024; (DBs, FCs, FBs); the maximum number of loadable blocks can be
DB	reduced by the MMC used.
Number, max.	1 024; Number range: 1 to 16000
• Size, max.	64 kbyte
FB	
Number, max.	1 024; Number range: 0 to 7999
• Size, max.	64 kbyte
FC	
Number, max.	1 024; Number range: 0 to 7999
• Size, max.	64 kbyte
OB	,
Number, max.	see instruction list
• Size, max.	64 kbyte
Number of free cycle OBs	1; OB 1
Number of time alarm OBs	1; OB 10
Number of delay alarm OBs	2; OB 20, 21
Number of cyclic interrupt OBs	4; OB 32, 33, 34, 35
Number of process alarm OBs	1; OB 40
Number of startup OBs	1; OB 100
Number of asynchronous error OBs	4; OB 80, 82, 85, 87
Number of asynchronous error OBs	2; OB 121, 122
Nesting depth	2, 05 121, 122
per priority class	16
additional within an error OB	4
Counters, timers and their retentivity	•
S7 counter	
• Number	256
Retentivity	250
•	Yes
— adjustable	
— preset	Z 0 to Z 7
Counting range	0
— lower limit	0
— upper limit	999
IEC counter	Voc
• present	Yes
• Type	SFB
• Number	Unlimited (limited only by RAM capacity)
S7 times	250
Number  Potentivity	256
Retentivity	Voc
— adjustable	Yes
— preset	No retentivity
Time range	40
— lower limit	10 ms
— upper limit	9 990 s
IEC timer	V
• present	Yes
• Type	SFB
<ul><li>Number</li></ul>	Unlimited (limited only by RAM capacity)

Data areas and their retentivity	
Retentive data area (incl. timers, counters, flags), max.	64 kbyte
Flag	
• Size, max.	256 byte
Retentivity available	Yes; MB 0 to MB 255
Retentivity preset	MB 0 to MB 15
Number of clock memories	8; 1 memory byte
Data blocks	7, 7, 7, 7, 7, 7, 7, 7, 7, 7, 7, 7, 7, 7
Retentivity adjustable	Yes; via non-retain property on DB
Retentivity preset	Yes
Local data	
per priority class, max.	32 kbyte; Max. 2048 bytes per block
Address area	
I/O address area	
• Inputs	1 024 byte
Outputs	1 024 byte
of which distributed	·
— Inputs	none
— Outputs	none
Process image	
• Inputs	1 024 byte
Outputs	1 024 byte
<ul> <li>Inputs, adjustable</li> </ul>	1 024 byte
Outputs, adjustable	1 024 byte
• Inputs, default	128 byte
Outputs, default	128 byte
Default addresses of the integrated channels	·
— Digital inputs	124.0 to 126.7
Digital outputs	124.0 to 125.7
— Analog inputs	752 to 761
— Analog outputs	752 to 755
Digital channels	
• Inputs	1 016
— of which central	1 016
<ul><li>Outputs</li></ul>	1 008
of which central	1 008
Analog channels	
• Inputs	253
— of which central	253
<ul><li>Outputs</li></ul>	250
— of which central	250
Hardware configuration	
Number of expansion units, max.	3
Number of DP masters	
integrated	none
• via CP	4
Number of operable FMs and CPs (recommended)	
• FM	8
• CP, PtP	8
• CP, LAN	10
Rack	
Racks, max.	4
Modules per rack, max.	8; In rack 3 max. 7
Time of day	
Clock	
Hardware clock (real-time)	Yes
retentive and synchronizable	Yes
Backup time	6 wk; At 40 °C ambient temperature
Deviation per day, max.	10 s; Typ.: 2 s
Behavior of the clock following POWER-ON	Clock continues running after POWER OFF

- Debaying of the cleak following evening of books a paried	the clock continues at the time of day it had when never was switched off
Behavior of the clock following expiry of backup period	the clock continues at the time of day it had when power was switched off
Operating hours counter	,
• Number	1
Number/Number range	0
Range of values	0 to 2^31 hours (when using SFC 101)
<ul> <li>Granularity</li> </ul>	1 h
retentive	Yes; Must be restarted at each restart
Clock synchronization	
<ul><li>supported</li></ul>	Yes
• to MPI, master	Yes
• on MPI, device	Yes
• in AS, master	Yes
• in AS, device	No
Digital inputs	
Number of digital inputs	24
<ul> <li>of which inputs usable for technological functions</li> </ul>	16
integrated channels (DI)	24
Input characteristic curve in accordance with IEC 61131, type 1	Yes
Number of simultaneously controllable inputs	
horizontal installation	
— up to 40 °C, max.	24
— up to 60 °C, max.	12
vertical installation	
— up to 40 °C, max.	12
Input voltage	
Rated value (DC)	24 V
• for signal "0"	-3 to +5V
• for signal "1"	+15 to +30 V
Input current	
• for signal "1", typ.	8 mA
Input delay (for rated value of input voltage)	
for standard inputs	
— parameterizable	Yes; 0.1 / 0.3 / 3 / 15 ms (You can reconfigure the input delay of the standard inputs during program runtime. Please note that under certain circumstances
Detect value	your newly set filter time may not be effective until the next filter cycle.)
— Rated value	3 ms
for technological functions	O Minimum and a middle facilities and a second a second and a second a second and a second a second and a
— at "0" to "1", max.	8 µs; Minimum pulse width/minimum pause between pulses at maximum counting frequency
Cable length	
shielded, max.	1 000 m; 50 m for technological functions
• unshielded, max.	600 m; for technological functions: No
for technological functions	555 m, for tearmological randuolis. No
— shielded, max.	50 m; at maximum count frequency
— snielded, max. — unshielded, max.	not allowed
— unsilielded, max.  Digital outputs	TIOT GILOWOU
	16
Number of digital outputs	16
of which high-speed outputs	4; Notice: You cannot connect the fast outputs of your CPU in parallel
integrated channels (DO)	16
Short-circuit protection	Yes; Clocked electronically
Response threshold, typ.	1 A
Limitation of inductive shutdown voltage to	L+ (-48 V)
Controlling a digital input	Yes
Switching capacity of the outputs	
on lamp load, max.	5 W
Load resistance range	
• lower limit	48 Ω
• upper limit	4 kΩ
Output voltage	
• for signal "1", min.	L+ (-0.8 V)
Output current	

for signal "1" rated value	500 mA
<ul><li>for signal "1" permissible range, min.</li></ul>	5 mA
<ul><li>for signal "1" permissible range, max.</li></ul>	0.6 A
<ul><li>for signal "1" minimum load current</li></ul>	5 mA
<ul><li>for signal "0" residual current, max.</li></ul>	0.5 mA
Parallel switching of two outputs	
• for uprating	No
<ul> <li>for redundant control of a load</li> </ul>	Yes
Switching frequency	
with resistive load, max.	100 Hz
<ul> <li>with inductive load, max.</li> </ul>	0.5 Hz
• on lamp load, max.	100 Hz
<ul> <li>of the pulse outputs, with resistive load, max.</li> </ul>	2.5 kHz
Total current of the outputs (per group)	
horizontal installation	
— up to 40 °C, max.	3 A
— up to 60 °C, max.	2 A
vertical installation	
— up to 40 °C, max.	2 A
— up to 40 °C, max.	
• shielded, max.	1 000 m
<ul><li>snieided, max.</li><li>unshielded, max.</li></ul>	1 000 m
	600 m
Analog inputs	-
Number of analog inputs	5
For voltage/current measurement	4
For resistance/resistance thermometer measurement	1
integrated channels (AI)	5; 4x current/voltage, 1x resistance
permissible input voltage for current input (destruction limit), max.	5 V; Permanent
permissible input voltage for voltage input (destruction limit), max.	30 V; Permanent
permissible input current for voltage input (destruction limit), max.	0.5 mA; Permanent
permissible input current for current input (destruction limit), max.	50 mA; Permanent
Electrical input frequency, max.	400 Hz
No-load voltage for resistance-type transmitter, typ.	3.3 V
Constant measurement current for resistance-type transmitter, typ.	1.25 mA
Technical unit for temperature measurement adjustable	Yes; Degrees Celsius / degrees Fahrenheit / Kelvin
Input ranges	
<ul> <li>Voltage</li> </ul>	Yes; ±10 V / 100 k $\Omega$ ; 0 V to 10 V / 100 k $\Omega$
Current	Yes; ±20 mA / 100 $\Omega$ ; 0 mA to 20 mA / 100 $\Omega$ ; 4 mA to 20 mA / 100 $\Omega$
Resistance thermometer	Yes; Pt 100 / 10 M $\Omega$
Resistance	Yes; 0 $\Omega$ to 600 $\Omega$ / 10 M $\Omega$
Input ranges (rated values), voltages	
• 0 to +10 V	Yes
— Input resistance (0 to 10 V)	100 kΩ
Input ranges (rated values), currents	
• 0 to 20 mA	Yes
— Input resistance (0 to 20 mA)	100 Ω
• -20 mA to +20 mA	Yes
— Input resistance (-20 mA to +20 mA)	100 Ω
• 4 mA to 20 mA	Yes
— Input resistance (4 mA to 20 mA)	100 Ω
Input ranges (rated values), resistance thermometer	100 12
• Pt 100	Yes
— Input resistance (Pt 100)	10 MΩ
	TO WISZ
Input ranges (rated values), resistors	Voc
• 0 to 600 ohms	Yes
— Input resistance (0 to 600 ohms)	10 ΜΩ
Thermocouple (TC)	

Temperature compensation	
— parameterizable	No
— parameterizatie  Characteristic linearization	
parameterizable	Yes; by software
parameterizable     — for resistance thermometer	Pt 100
Cable length	Ft 100
•	400 m
• shielded, max.	100 m
Analog outputs	
integrated channels (AO)	2
Voltage output, short-circuit protection	Yes
Voltage output, short-circuit current, max.	55 mA
Current output, no-load voltage, max.	14 V
Output ranges, voltage	· ·
• 0 to 10 V	Yes
• -10 V to +10 V	Yes
Output ranges, current	
• 0 to 20 mA	Yes
• -20 mA to +20 mA	Yes
• 4 mA to 20 mA	Yes
Connection of actuators	
<ul> <li>for voltage output two-wire connection</li> </ul>	Yes; Without compensation of the line resistances
<ul> <li>for voltage output four-wire connection</li> </ul>	No
for current output two-wire connection	Yes
Load impedance (in rated range of output)	
<ul><li>with voltage outputs, min.</li></ul>	1 kΩ
<ul> <li>with voltage outputs, capacitive load, max.</li> </ul>	0.1 µF
<ul> <li>with current outputs, max.</li> </ul>	300 Ω
<ul> <li>with current outputs, inductive load, max.</li> </ul>	0.1 mH
Destruction limits against externally applied voltages and currents	
<ul> <li>Voltages at the outputs towards MANA</li> </ul>	16 V; Permanent
<ul><li>Current, max.</li></ul>	50 mA; Permanent
Cable length	
• shielded, max.	200 m
Analog value generation for the inputs	
Measurement principle	Actual value encryption (successive approximation)
measurement principle	**
·	
Integration and conversion time/resolution per channel	12 bit
Integration and conversion time/resolution per channel  Resolution with overrange (bit including sign), max.	
Integration and conversion time/resolution per channel	12 bit Yes; 16.6 / 20 ms 50 / 60 Hz
Integration and conversion time/resolution per channel  Resolution with overrange (bit including sign), max.  Integration time, parameterizable  Interference voltage suppression for interference	Yes; 16.6 / 20 ms
Integration and conversion time/resolution per channel  Resolution with overrange (bit including sign), max.  Integration time, parameterizable  Interference voltage suppression for interference frequency f1 in Hz	Yes; 16.6 / 20 ms 50 / 60 Hz
Integration and conversion time/resolution per channel  Resolution with overrange (bit including sign), max.  Integration time, parameterizable  Interference voltage suppression for interference frequency f1 in Hz  Time constant of the input filter	Yes; 16.6 / 20 ms 50 / 60 Hz 0.38 ms
Integration and conversion time/resolution per channel  Resolution with overrange (bit including sign), max.  Integration time, parameterizable  Interference voltage suppression for interference frequency f1 in Hz  Time constant of the input filter  Basic execution time of the module (all channels	Yes; 16.6 / 20 ms 50 / 60 Hz 0.38 ms
Integration and conversion time/resolution per channel  Resolution with overrange (bit including sign), max.  Integration time, parameterizable  Interference voltage suppression for interference frequency f1 in Hz  Time constant of the input filter  Basic execution time of the module (all channels released)	Yes; 16.6 / 20 ms 50 / 60 Hz 0.38 ms
Integration and conversion time/resolution per channel  Resolution with overrange (bit including sign), max.  Integration time, parameterizable  Interference voltage suppression for interference frequency f1 in Hz  Time constant of the input filter  Basic execution time of the module (all channels released)  Analog value generation for the outputs	Yes; 16.6 / 20 ms 50 / 60 Hz 0.38 ms
Integration and conversion time/resolution per channel  Resolution with overrange (bit including sign), max.  Integration time, parameterizable  Interference voltage suppression for interference frequency f1 in Hz  Time constant of the input filter  Basic execution time of the module (all channels released)  Analog value generation for the outputs  Integration and conversion time/resolution per channel	Yes; 16.6 / 20 ms 50 / 60 Hz  0.38 ms 1 ms
Integration and conversion time/resolution per channel  Resolution with overrange (bit including sign), max.  Integration time, parameterizable  Interference voltage suppression for interference frequency f1 in Hz  Time constant of the input filter  Basic execution time of the module (all channels released)  Analog value generation for the outputs  Integration and conversion time/resolution per channel  Resolution with overrange (bit including sign), max.	Yes; 16.6 / 20 ms 50 / 60 Hz  0.38 ms 1 ms
Integration and conversion time/resolution per channel  Resolution with overrange (bit including sign), max.  Integration time, parameterizable  Interference voltage suppression for interference frequency f1 in Hz  Time constant of the input filter  Basic execution time of the module (all channels released)  Analog value generation for the outputs  Integration and conversion time/resolution per channel  Resolution with overrange (bit including sign), max.  Conversion time (per channel)	Yes; 16.6 / 20 ms 50 / 60 Hz  0.38 ms 1 ms
Integration and conversion time/resolution per channel  Resolution with overrange (bit including sign), max.  Integration time, parameterizable  Interference voltage suppression for interference frequency f1 in Hz  Time constant of the input filter  Basic execution time of the module (all channels released)  Analog value generation for the outputs  Integration and conversion time/resolution per channel  Resolution with overrange (bit including sign), max.  Conversion time (per channel)  Settling time	Yes; 16.6 / 20 ms 50 / 60 Hz  0.38 ms 1 ms  12 bit 1 ms
Integration and conversion time/resolution per channel  Resolution with overrange (bit including sign), max.  Integration time, parameterizable  Interference voltage suppression for interference frequency f1 in Hz  Time constant of the input filter  Basic execution time of the module (all channels released)  Analog value generation for the outputs  Integration and conversion time/resolution per channel  Resolution with overrange (bit including sign), max.  Conversion time (per channel)  Settling time  for resistive load	Yes; 16.6 / 20 ms 50 / 60 Hz  0.38 ms 1 ms  12 bit 1 ms  0.6 ms
Integration and conversion time/resolution per channel  Resolution with overrange (bit including sign), max.  Integration time, parameterizable  Interference voltage suppression for interference frequency f1 in Hz  Time constant of the input filter  Basic execution time of the module (all channels released)  Analog value generation for the outputs  Integration and conversion time/resolution per channel  Resolution with overrange (bit including sign), max.  Conversion time (per channel)  Settling time  for resistive load  for capacitive load	Yes; 16.6 / 20 ms 50 / 60 Hz  0.38 ms 1 ms  12 bit 1 ms  0.6 ms 1 ms
Integration and conversion time/resolution per channel  Resolution with overrange (bit including sign), max.  Integration time, parameterizable  Interference voltage suppression for interference frequency f1 in Hz  Time constant of the input filter  Basic execution time of the module (all channels released)  Analog value generation for the outputs  Integration and conversion time/resolution per channel  Resolution with overrange (bit including sign), max.  Conversion time (per channel)  Settling time  for resistive load  for capacitive load  for inductive load	Yes; 16.6 / 20 ms 50 / 60 Hz  0.38 ms 1 ms  12 bit 1 ms  0.6 ms 1 ms
Integration and conversion time/resolution per channel  Resolution with overrange (bit including sign), max.  Integration time, parameterizable  Interference voltage suppression for interference frequency f1 in Hz  Time constant of the input filter  Basic execution time of the module (all channels released)  Analog value generation for the outputs  Integration and conversion time/resolution per channel  Resolution with overrange (bit including sign), max.  Conversion time (per channel)  Settling time  for resistive load  for capacitive load  for inductive load  Encoder	Yes; 16.6 / 20 ms 50 / 60 Hz  0.38 ms 1 ms  12 bit 1 ms  0.6 ms 1 ms
Integration and conversion time/resolution per channel  Resolution with overrange (bit including sign), max.  Integration time, parameterizable  Interference voltage suppression for interference frequency f1 in Hz  Time constant of the input filter  Basic execution time of the module (all channels released)  Analog value generation for the outputs  Integration and conversion time/resolution per channel  Resolution with overrange (bit including sign), max.  Conversion time (per channel)  Settling time  for resistive load  for capacitive load  for inductive load  Connection of signal encoders	Yes; 16.6 / 20 ms 50 / 60 Hz  0.38 ms 1 ms  12 bit 1 ms  0.6 ms 1 ms 0.5 ms
Integration and conversion time/resolution per channel  Resolution with overrange (bit including sign), max.  Integration time, parameterizable  Interference voltage suppression for interference frequency f1 in Hz  Time constant of the input filter  Basic execution time of the module (all channels released)  Analog value generation for the outputs  Integration and conversion time/resolution per channel  Resolution with overrange (bit including sign), max.  Conversion time (per channel)  Settling time  for resistive load  for capacitive load  for inductive load  for inductive load  Encoder  Connection of signal encoders  for voltage measurement  for current measurement as 2-wire transducer	Yes; 16.6 / 20 ms 50 / 60 Hz  0.38 ms 1 ms  12 bit 1 ms  0.6 ms 1 ms 0.5 ms  Yes Yes; with external supply
Integration and conversion time/resolution per channel  Resolution with overrange (bit including sign), max.  Integration time, parameterizable  Interference voltage suppression for interference frequency f1 in Hz  Time constant of the input filter  Basic execution time of the module (all channels released)  Analog value generation for the outputs  Integration and conversion time/resolution per channel  Resolution with overrange (bit including sign), max.  Conversion time (per channel)  Settling time  for resistive load  for capacitive load  for inductive load  for inductive load  Encoder  Connection of signal encoders  for voltage measurement  for current measurement as 2-wire transducer  for current measurement as 4-wire transducer	Yes; 16.6 / 20 ms 50 / 60 Hz  0.38 ms 1 ms  12 bit 1 ms  0.6 ms 1 ms 0.5 ms  Yes Yes; with external supply Yes
Integration and conversion time/resolution per channel  Resolution with overrange (bit including sign), max.  Integration time, parameterizable  Interference voltage suppression for interference frequency f1 in Hz  Time constant of the input filter  Basic execution time of the module (all channels released)  Analog value generation for the outputs  Integration and conversion time/resolution per channel  Resolution with overrange (bit including sign), max.  Conversion time (per channel)  Settling time  for resistive load  for capacitive load  for inductive load  Encoder  Connection of signal encoders  for voltage measurement  for current measurement as 2-wire transducer  for current measurement as 4-wire transducer  for resistance measurement with two-wire connection	Yes; 16.6 / 20 ms 50 / 60 Hz  0.38 ms 1 ms  12 bit 1 ms  0.6 ms 1 ms 0.5 ms  Yes Yes; with external supply Yes Yes; Without compensation of the line resistances
Integration and conversion time/resolution per channel  Resolution with overrange (bit including sign), max.  Integration time, parameterizable  Interference voltage suppression for interference frequency f1 in Hz  Time constant of the input filter  Basic execution time of the module (all channels released)  Analog value generation for the outputs  Integration and conversion time/resolution per channel  Resolution with overrange (bit including sign), max.  Conversion time (per channel)  Settling time  for resistive load  for capacitive load  for inductive load  for inductive load  for voltage measurement  for current measurement as 2-wire transducer  for current measurement as 4-wire transducer  for resistance measurement with two-wire connection  for resistance measurement with three-wire connection	Yes; 16.6 / 20 ms 50 / 60 Hz  0.38 ms 1 ms  12 bit 1 ms  0.6 ms 1 ms 0.5 ms  Yes Yes; with external supply Yes Yes; Without compensation of the line resistances No
Integration and conversion time/resolution per channel  Resolution with overrange (bit including sign), max.  Integration time, parameterizable  Interference voltage suppression for interference frequency f1 in Hz  Time constant of the input filter  Basic execution time of the module (all channels released)  Analog value generation for the outputs  Integration and conversion time/resolution per channel  Resolution with overrange (bit including sign), max.  Conversion time (per channel)  Settling time  for resistive load  for capacitive load  for inductive load  for inductive load  for outrent measurement  for current measurement as 2-wire transducer  for current measurement as 4-wire transducer  for resistance measurement with two-wire connection  for resistance measurement with four-wire connection	Yes; 16.6 / 20 ms 50 / 60 Hz  0.38 ms 1 ms  12 bit 1 ms  0.6 ms 1 ms 0.5 ms  Yes Yes; with external supply Yes Yes; Without compensation of the line resistances
Integration and conversion time/resolution per channel  Resolution with overrange (bit including sign), max.  Integration time, parameterizable  Interference voltage suppression for interference frequency f1 in Hz  Time constant of the input filter  Basic execution time of the module (all channels released)  Analog value generation for the outputs  Integration and conversion time/resolution per channel  Resolution with overrange (bit including sign), max.  Conversion time (per channel)  Settling time  for resistive load  for capacitive load  for inductive load  for inductive load  for voltage measurement  for current measurement as 2-wire transducer  for current measurement as 4-wire transducer  for resistance measurement with two-wire connection  for resistance measurement with three-wire connection	Yes; 16.6 / 20 ms 50 / 60 Hz  0.38 ms 1 ms  12 bit 1 ms  0.6 ms 1 ms 0.5 ms  Yes Yes; with external supply Yes Yes; Without compensation of the line resistances No

permissible quiescent current (2-wire sensor), max.	1.5 mA
— permissible quiescent current (z-wire sensor), max.  Errors/accuracies	1.0 110 1
Temperature error (relative to input range), (+/-)	0.006 %/K
Crosstalk between the inputs, min.	60 dB
Repeat accuracy in steady state at 25 °C (relative to input range), (+/-)	0.06 %
Output ripple (relative to output range, bandwidth 0 to 50 kHz), (+/-)	0.1 %
Linearity error (relative to output range), (+/-)	0.15 %
Temperature error (relative to output range), (+/-)	0.01 %/K
Crosstalk between the outputs, min.	60 dB
Repeat accuracy in steady state at 25 °C (relative to output range), (+/-)	0.06 %
Operational error limit in overall temperature range	
<ul> <li>Voltage, relative to input range, (+/-)</li> </ul>	1 %
<ul> <li>Current, relative to input range, (+/-)</li> </ul>	1 %
<ul> <li>Resistance, relative to input range, (+/-)</li> </ul>	1 %
<ul> <li>Voltage, relative to output range, (+/-)</li> </ul>	1 %
Current, relative to output range, (+/-)	1 %
Basic error limit (operational limit at 25 °C)	
Voltage, relative to input range, (+/-)	0.8 %; Linearity error ±0.06 %
• Current, relative to input range, (+/-)	0.8 %; Linearity error ±0.06 %
Resistance, relative to input range, (+/-)  Pariety as the appropriate probability as input appropriate (+/-)  The single probability and the probability are input appropriate.	0.8 %; Linearity error ±0.2 %
Resistance thermometer, relative to input range, (+/-)      Veltage relative to extract range, (+/-)	0.8 %
Voltage, relative to output range, (+/-)     Current relative to output range, (+/-)	0.8 %
• Current, relative to output range, (+/-)	0.8 %
<ul> <li>Interference voltage suppression for f = n x (f1 +/- 1 %), f1 = interference</li> <li>Series mode interference (peak value of interference &lt; rated value of input range), min</li> </ul>	30 dB
rated value of input range), min.  • Common mode interference, min.	40 dB
Interfaces	40 db
Number of PROFINET interfaces	0
Number of RS 485 interfaces	1; MPI
Number of RS 422 interfaces	1; RS 422 / 485 combined
Point-to-point connection	1, 110 122 / 100 0011billiou
Cable length, max.	1 200 m
Integrated protocol driver	
— 3964 (R)	Yes
— ASCII	Yes
— RK 512	Yes
Transmission rate, RS 422/485	
— with 3964 (R) protocol, max.	19.2 kbit/s; 38.4 kbit/s half duplex; 19.2 kbit/s full duplex
— with ASCII protocol, max.	19.2 kbit/s; 38.4 kbit/s half duplex; 19.2 kbit/s full duplex
— with RK 512 protocol, max.	19.2 kbit/s; 38.4 kbit/s half duplex; 19.2 kbit/s full duplex
1. Interface	
Interface type	Integrated RS 485 interface
Isolated	No
Interface types	
• RS 485	Yes
Output current of the interface, max.	200 mA
Protocols	
• MPI	Yes
PROFIBUS DP master	No
PROFIBUS DP device	No
Point-to-point connection	No
MPI	
Transmission rate, max.	187.5 kbit/s
Services	
— PG/OP communication	Yes
— Routing	No Yes
— Global data communication	Yes
— S7 basic communication	Yes

— S7 communication	Yes; Only server, configured on one side
— S7 communication  — S7 communication, as client	No; but via CP and loadable FB
— S7 communication, as server	Yes
2. Interface	
Interface type	Integrated RS 422/ 485 interface
Isolated	Yes
Interface types	103
• RS 485	Yes; RS 422 / 485 (X.27)
Output current of the interface, max.	No
Protocols	
• MPI	No
PROFINET IO Controller	No
PROFINET IO Device	No
PROFINET CBA	No
PROFIBUS DP master	No
PROFIBUS DP device	No
Point-to-point connection	Yes
Point-to-point connection	
Transmission rate, max.	19.2 kbit/s; 38.4 kbit/s half duplex; 19.2 kbit/s full duplex
Interface controllable from the user program	Yes
Interface controllable from the user program     Interface can trigger alarm/interrupt in the user program	Yes; Message on break - identification
Protocols	.,
PROFIsafe	No
communication functions / header	
PG/OP communication	Yes
Data record routing	No
Global data communication	
• supported	Yes
Number of GD loops, max.	8
Number of GD packets, max.	8
Number of GD packets, max.  Number of GD packets, transmitter, max.	8
Number of GD packets, transmitter, max.     Number of GD packets, receiver, max.	8
Size of GD packets, max.	22 byte
Size of GD packet (of which consistent), max.	22 byte
S7 basic communication	
• supported	Yes
User data per job, max.	76 byte
User data per job (of which consistent), max.	76 byte; 76 bytes (with X SEND or X RCV); 64 bytes (with X PUT or X GET
	as server)
S7 communication	
• supported	Yes
• as server	Yes
• as client	Yes; Via CP and loadable FB
<ul> <li>User data per job, max.</li> </ul>	180 kbyte; With PUT/GET
<ul> <li>User data per job (of which consistent), max.</li> </ul>	240 byte; as server
S5 compatible communication	
• supported	Yes; via CP and loadable FC
Number of connections	
• overall	12
<ul> <li>usable for PG communication</li> </ul>	11
<ul> <li>reserved for PG communication</li> </ul>	1
<ul><li>— adjustable for PG communication, min.</li></ul>	1
<ul> <li>adjustable for PG communication, max.</li> </ul>	11
• usable for OP communication	11
<ul> <li>reserved for OP communication</li> </ul>	1
<ul><li>— adjustable for OP communication, min.</li></ul>	1
<ul> <li>adjustable for OP communication, max.</li> </ul>	11
<ul> <li>usable for S7 basic communication</li> </ul>	8
<ul> <li>reserved for S7 basic communication</li> </ul>	0
<ul> <li>adjustable for S7 basic communication, min.</li> </ul>	0

Number of login stations for message functions, max.	12; Depending on the configured connections for PG/OP and S7 basic communication
Process diagnostic messages	Yes
simultaneously active Alarm-S blocks, max.	300
est commissioning functions	
Status block	Yes; Up to 2 simultaneously
Single step	Yes
Number of breakpoints	4
Status/control	
Status/control variable	Yes
Variables	
	Inputs, outputs, memory bits, DB, times, counters 30
Number of variables, max.  of which status variables, max.	30
— of which status variables, max.	14
— of which control variables, max.	14
Forcing	Vee
• Forcing	Yes
• Forcing, variables	Inputs, outputs
Number of variables, max.	10
Diagnostic buffer	
• present	Yes
<ul> <li>Number of entries, max.</li> </ul>	500
— adjustable	No
<ul><li>of which powerfail-proof</li></ul>	100; Only the last 100 entries are retained
<ul> <li>Number of entries readable in RUN, max.</li> </ul>	499
— adjustable	Yes; From 10 to 499
— preset	10
Service data	
• can be read out	Yes
nterrupts/diagnostics/status information	
Diagnostics indication LED	
<ul> <li>Status indicator digital input (green)</li> </ul>	Yes
<ul> <li>Status indicator digital output (green)</li> </ul>	Yes
ntegrated Functions	
Counter	
Number of counters	4; See "Technological Functions" manual
<ul> <li>Counting frequency, max.</li> </ul>	60 kHz
Frequency measurement	Yes
Number of frequency meters	4; up to 60 kHz (see "Technological Functions" manual)
controlled positioning	Yes
integrated function blocks (closed-loop control)	Yes; PID controller (see "Technological Functions" manual)
PID controller	Yes
Number of pulse outputs	4; Pulse width modulation up to 2.5 kHz (see "Technological Functions"
realiser of pulse outputs	Manual)
Limit frequency (pulse)	2.5 kHz
otential separation	
Potential separation digital inputs	
Potential separation digital inputs	Yes
between the channels	No
between the channels and backplane bus	Yes
Potential separation digital outputs	100
	Yes
Potential separation digital outputs     Potential separation digital outputs	
between the channels     between the channels in groups of	Yes
between the channels, in groups of	8
between the channels and backplane bus	Yes
Potential separation analog inputs	
<ul> <li>Potential separation analog inputs</li> </ul>	Yes; common for analog I/O
<ul> <li>between the channels</li> </ul>	No
between the channels and backplane bus	Yes
Potential separation analog outputs	
	Yes; common for analog I/O

• between the channels	No
between the channels and backplane bus	Yes
Isolation	
Isolation tested with	600 V DC
Ambient conditions	
Ambient temperature during operation	
• min.	0 °C
• max.	60 °C
configuration / header	
Configuration software	
• STEP 7	Yes; STEP 7 V5.5 + SP1 or higher or STEP 7 V5.3 + SP2 or higher with HSP 203
STEP 7 Lite	No
configuration / programming / header	
<ul> <li>Command set</li> </ul>	see instruction list
Nesting levels	8
<ul> <li>System functions (SFC)</li> </ul>	see instruction list
<ul> <li>System function blocks (SFB)</li> </ul>	see instruction list
Programming language	
— LAD	Yes
— FBD	Yes
— STL	Yes
— SCL	Yes
— CFC	Yes
— GRAPH	Yes
— HiGraph®	Yes
Know-how protection	
<ul> <li>User program protection/password protection</li> </ul>	Yes
Block encryption	Yes; With S7 block Privacy
Dimensions	
Width	120 mm
Height	125 mm
Depth	130 mm
Weights	
Weight, approx.	680 g

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